

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: K. ITOH, et al.

Application No.: Rule 1.53(b) Continuation of U.S. Patent Application  
Serial No. 10/330,077, filed December 30, 2002

Filed: On Even Date Herewith

For: SEMICONDUCTOR INTEGRATED CIRCUIT  
DEVICE WITH IMPROVED STORAGE MOSFET  
ARRANGEMENT (As Amended)

Art Group of Parent: 2818

Examiner of Parent: G. N. Auduong

**PRELIMINARY AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

March 10, 2004

Sir:

Prior to examination, please amend the above-identified application as listed below and as set forth on the following pages:

**Amendments to the Title and Specification and**

**Remarks are included following the amendments.**